



PATENT APPLICATION

Attorney Docket No.: 9898-168
Applicant: Sang-hoon LEE
Filing Date: October 25, 2000

Serial No.: 09/696,816

Group Art No.: ~~Not yet assigned~~ 2125INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

<u>Exam. Int.</u>	<u>Ref.</u>	<u>Document No.</u>	<u>Date</u>	<u>Name</u>	<u>Sub Class</u>	<u>Class</u>	<u>Filing Date</u>
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FOREIGN PATENT DOCUMENTS

<u>Application No.</u>	<u>Date</u>	<u>Filing Date</u>	<u>Inventor</u>
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OTHER DOCUMENTS

<u>Exam. Int.</u>	<u>Ref.</u>	<u>(Including Author, Title, Date Pertinent Pages, Etc.)</u>
QvP		Narain Arora, "MOSFET MODELS FOR VLSI CIRCUIT SIMULATION THEORY AND PRACTICE", 1993, pages 510-535.
QvP		James C. Chen et al., "E-T BASED STATISTICAL MODELING AND COMPACT STATISTICAL CIRCUIT SIMULATION METHODOLOGIES", 1996 IEEE, pages 23.6.1-23.6.4

Examiner: Albert R. PaladiniALBERT R. PALADINI
PATENT EXAMINERDate Considered: 1-14-04



COPY OF PAPER
ORIGINALLY FILED PATENT APPLICATION

Atty. Docket No.:

9898-108

Serial No.: 09/696,816

Applicant:

Sang-hoon Lee

Filing Date:

October 25, 2000

Group:

2125
2123

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U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-</u> <u>Class</u>
QW		5,790,436	Aug. 4, 1998	Chen, et al.		

OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, etc.)</u>
QW		Star-Hspice User's Manual, Release 1998.2, "Analyzing Electrical Yields," (10-31 to 10-38), 8 pages

Examiner:

ALBERT W. PALADIN
PATENT EXAMINER

Date Considered: 1-14-04